

MAR 28 2005

PATENT

Docket No.: JCLA8532

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Applicant : MOU-SHIUNG LIN et al.
Application No. : 10/055,560
Filed : 1/22/2002
For : INTEGRATED CHIP PACKAGE STRUCTURE USING METAL
SUBSTRATE AND METHOD OF MANUFACTURING THE SAME
Group Art Unit : 2827

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

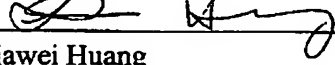
Enclosed herewith is Form PTO-1449 listing ONE (1) U.S. reference that first page of which is also enclosed. This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(c), i.e. after a first Office Action is sent but before a final Office Action or a Notice of Allowance is sent.

The Commissioner is authorized to charge necessary fee in the amount of \$ 180 for covering the fee set forth in 37 CFR §1.17(p) or any other fees required in connection with the filing of this paper to our Deposit Account No. 50-0710 (Order No. JCLA8532).

The submission of the references should not be interpreted as admitting them as prior art.

Respectfully submitted,
J.C. PATENTS

Dated: 3/28/2005

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT	ATTY. DOCKET NO.: JCLA8532	APPLICATION No.: 10/055,560
	APPLICANT: MOU-SHIUNG LIN et al.	
	FILING DATE: January 22, 2002	GROUP: 2827

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
	6,121,688	09/19/00	Masatoshi Akagawa	257	778	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

EXAMINER	DATE CONSIDERED
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EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED. INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.